

## **Integrating Modular Strategies into an Embedded System Design Laboratory: An Outcome-Based Educational Approach**

**Danilo Rojas<sup>1</sup>, Manuel Jiménez<sup>1</sup>, and Aidsa I. Santiago-Román<sup>2</sup>**

*<sup>1</sup>Computer and Electrical Engineering/<sup>2</sup>Engineering Sciences and Materials*

*University of Puerto Rico at Mayaguez*

*Mayagüez, PR, 00681-9000*

*danilo.rojas@upr.edu*

### **Abstract**

Teaching embedded systems design and enhancing student skills in this area is an important task in computer engineering programs to provide an up to date education. This work focuses on the development of a teaching methodology based on a modular design, aided by an Outcome-Based educational framework. To achieve this objective, the content, pedagogical, and assessments activities for an embedded system design laboratory were aligned to ensure proper student learning. The pedagogical methods were designed based on modular strategies through progressive lab experiences. The proposed methodology is currently in the process of validation through a performance comparison between students who took the course in previous semesters (control) and those currently taking it (experimental). At the moment, the data for the control group have been collected and analyzed while the data for the experimental group is being collected.

### **Keywords**

Modular design, outcome-based education, embedded system education.

### **Introduction**

The average American interacts with as many as one hundred embedded systems per day<sup>1</sup>. This number continues to grow as integration technologies continue to develop. This growth denotes the relevance of embedded systems in our daily lives and highlights the importance to provide a solid and up to date education in this area to future computer engineers (CE).

Due to the relevance of embedded systems (ES) in CE education, courses in this area are considered core knowledge in most computer engineering programs. Different methodologies have been developed and implemented to teach the fundamental ES concepts and at the same time enhance student's practical skills in this area. Some of these methodologies use problems<sup>2-4</sup>, projects<sup>5-7</sup>, video games<sup>8,9</sup> or virtual labs<sup>10,11</sup>, among others, to attract and motivate students in their learning. Furthermore, well-designed courses need to cover not only aspect related to embedded systems architecture and interfacing but also on the design process. This paper proposes a strategy based on modular design technologies and an Outcome-Based educational (OBE) approach to teach embedded systems design concepts in an applied laboratory.

The rest of this paper is organized as follows. The OBE framework implementation is briefly explained and the modular design approach is introduced in the next section. Subsequently, the

results collected for the control and experimental groups are discussed. Finally, a summary of the continuing work is presented in the last section.

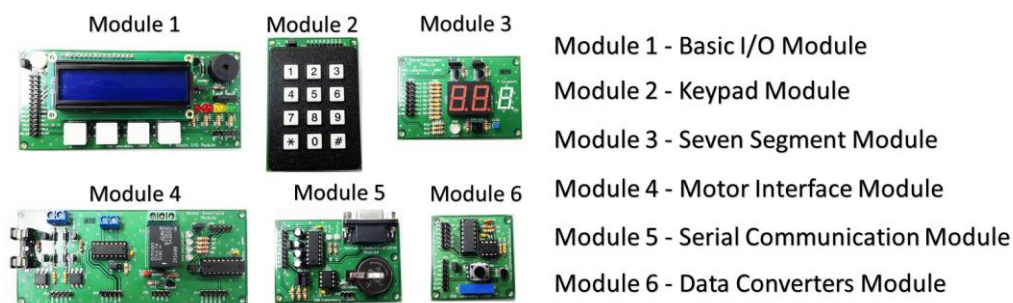
### Laboratory Structure Redesign

An OBE framework proposed by Streveler et. al.<sup>12</sup> was implemented in the Embedded System Design laboratory at the University of Puerto Rico, Mayagüez campus to provide for a structured student learning process. This framework attempts to align content, pedagogical, and assessment methods in order to obtain a well-structured laboratory experience.

The proposed laboratory content was revised based on a desired student profile for a computer engineering graduate. This profile took into consideration the current Curriculum Guidelines for Undergraduate Degree Programs in Computer Engineering<sup>13</sup>, an analysis of social and industrial expectations, and the current departmental focus for the computer engineering academic program. At the end of this review process, the proposed content was summarized in a series of learning objectives that students need to meet at the end of the class.

Once the content was designed, the pedagogical methods were implemented using a modular design approach in which students worked with a progressive series of lab experiments and a set of functional circuits modules (educational modules). The progression of experiments was structured to use, in each new unit, the abilities learned in previous experiments. A total of eight experiments were designed where each unit was organized in four dedicated sections: objectives, lecture, basic exercises, and a complementary task.

A total of six different modules were developed to fulfill the technical objectives of each experiment (See Figure 1). These modules were constructed based on the topics and circuits established for the eight experiments, where each module could be used in one or more experiments. Each module provides students with an example of how electronics modules are structured including functional diagrams, schematics, board design, and software usage guidelines, allowing for their combined use with a target controller to develop target applications.



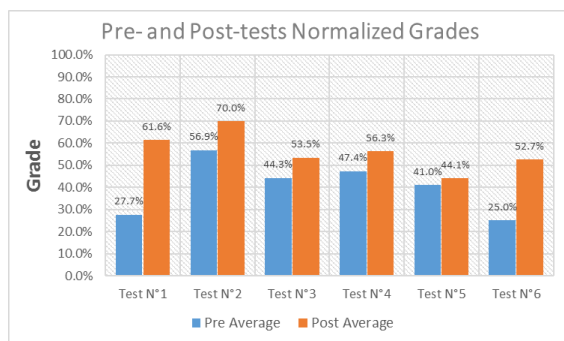
**Figure 1: Educational modules developed**

The OBE included assessment methods to validate the methodology and to assess how well each student met the learning objectives defined in the content phase. The assessment methods included a series of pre- and post-tests given to students in each experiment to quantify the prior

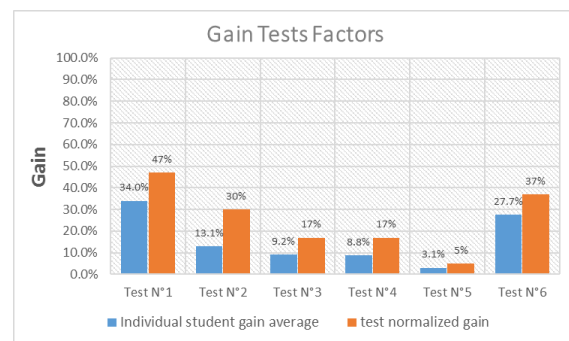
and gained knowledge. Each test was subjected to a validation process to determine its item difficulty, discrimination, and reliability index. Also, learning gain factors were computed for each test and student, in order to conduct a performance comparison between both student groups. The first of the groups corresponds to students who took the class with the previous methodology (control group) and the second group is formed by students who are taking the class with the proposed methodology (experimental group).

### Preliminary Tendencies

Currently, the data collection process for the control group was completed. These data correspond to six different experiments where a total of sixteen students were evaluated. Figure 2 shows the current grades for the pre- and post-tests in a normalized scale while Figure 3 shows the learning gain factors for the tests.



**Figure 2: Control group tests grades**



**Figure 3: Control group tests gain factors**

From Figure 2, we can observe that the pre-test grades were lower than those in the post-tests, meaning that students improved their knowledge after experiencing the laboratory practice. Although they had an increase in knowledge, the post-test average (56.3%) was under the threshold defined by the Computer Engineering department to define if a student passes a course (70.0%). Figure 3 shows the learning gain trend for the tests. From this Figure, we notice that tests (N°1, N°2, and N°6) can be considered effective by getting learning gain factors above 30%<sup>14</sup>.

### Summary and Ongoing Work

A new teaching methodology based on modular design and OBE framework for an embedded system design laboratory was presented. The OBE framework implementation was conducted making emphasis on how: (1) the content was established, (2) the methodology was selected, and (3) the assessment methods were designed. Also, the implementation of modular design concepts through the use of progressive experiments and modules design was explained. Currently, the results for the control group have been obtained and analyzed, showing that there is an opportunity for improvement in the laboratory pedagogy and the way in which students learn concepts about embedded systems. For this reason, once the results for the experimental group are obtained, we expect to see an increment in the post-tests grades and an increment in the student learning gain factors. This shall provide the evidence to support the proposed methodology. Our expectation is that if our hypothesis were confirmed, the proposed methodology could be implemented in other courses.

## References

- 1 W. Wolf and J. Madsen, "Embedded systems education for the future," *Proceedings of the IEEE*, vol. 88, no. 1, pp. 23–30, Jan 2000.
- 2 D. L. Maskell and P. J. Grabau, "A multidisciplinary cooperative problem-based learning approach to embedded systems design," *Education, IEEE Transactions on*, vol. 41, no. 2, pp. 101–103, 1998
- 3 J. W. Bruce, J. C. Harden, and R. B. Reese, "Cooperative and progressive design experience for embedded systems," *Education, IEEE Transactions on*, vol. 47, no. 1, pp. 83–92, 2004.
- 4 L. Ordinez and O. Alimenti, "A constructivist approach for teaching embedded systems," *Latin America Transactions, IEEE (Revista IEEE America Latina)*, vol. 11, no. 1, pp. 572–578, Feb 2013.
- 5 C.-S. Lee, J.-H. Su, K.-E. Lin, J.-H. Chang, and G.-H. Lin, "A project-based laboratory for learning embedded system design with industry support," *Education, IEEE Transactions on*, vol. 53, no. 2, pp. 173–181, May 2010.
- 6 A. Kumar, S. Fernando, and R. Panicker, "Project-based learning in embedded systems education using an fpga platform," *Education, IEEE Transactions on*, vol. 56, no. 4, pp. 407–415, Nov 2013.
- 7 I. Couvertier, M. Jimenez, R. Palomera, and M. Toledo, "Integrating concepts and practice in teaching embedded systems design," in *Proceedings of the International Conference on Engineering Education (ICEE 2004)*, Gainesville, FL, 2004.
- 8 J. Gonzalez, P. Pomares, M. Damas, P. Garcia-Sanchez, M. Rodriguez-Alvarez, and J. M. Palomares, "The use of video-gaming devices as a motivation for learning embedded systems programming," *Education, IEEE Transactions on*, vol. 56, no. 2, pp. 199–207, 2013.
- 9 U. Münz, P. Schumm, A. Wiesebrock, and F. Allgöwer, "Motivation and learning progress through educational games," *Industrial Electronics, IEEE Transactions on*, vol. 54, no. 6, pp. 3141–3144, 2007.
- 10 T. Kodama, Y. Suzuki, and S. Chiba, "Development of a remote practice system for embedded system education," in *Mechatronics and Embedded Systems and Applications (MESA), 2010 IEEE/ASME International Conference on*. IEEE, 2010, pp. 53–58.
- 11 S. Buchner and S. Jaschke, "Preparation for embedded systems laboratories the virtual workspace approach," in *Global Engineering Education Conference (EDUCON), 2013 IEEE*, March 2013, pp. 171–175
- 12 R. A. Streveler, K. A. Smith, and M. Pilotte, "Aligning course content, assessment, and delivery: Creating a context for outcome-based education," Hershey, Pennsylvania: IGI Global, 2012
- 13 D. Soldan, J. Hughes, J. Impagliazzo, A. McGettrick, V. P. Nelson, P. K. Srimani, and M. D. Theys, "Curriculum guidelines for undergraduate degree programs in computer engineering," Retrieved December, 2004.
- 14 R. R. Hake, "Interactive-engagement versus traditional methods: A six thousand-student survey of mechanics test data for introductory physics courses," *American Journal of Physics*, vol. 66, no. 1, 1998.

## Danilo Rojas

Danilo A. Rojas Méndez is a graduate student in the Electrical and Computer Engineering (ECE) Department at the University of Puerto Rico, Mayaguez Campus (UPRM). Danilo Rojas earned a BA in Mechatronic Engineering from Universidad Autonoma del Caribe at Barranquilla-Colombia. He has been working as teaching assistant for the Embedded Systems Design laboratory for the last two years. He also works as a research assistant in a project for developing untethered underwater vehicles.

## Manuel Jiménez

Professor Jiménez has over twenty-five years of experience teaching and developing practical embedded systems applications. After earning his Ph.D. degree in Electrical Engineering from Michigan State University, he joined the UPRM where he currently holds a position as professor in the Electrical and Computer Engineering Department. He is currently the Associate Dean of Engineering Academic Affairs at the UPRM. His current research interests include embedded

systems design, rapid systems prototyping, and automated characterization of electronic devices and systems. The work by professor Jiménez has produced over one hundred technical articles published in conferences and journals around the world and he is the leading author of the Springer textbook “Introduction to Embedded Systems”. Professor Jiménez is a Senior IEEE member.

**Aidsa Santiago-Román**

Dr. Aidsa I. Santiago-Román is an Associate Professor in the Engineering Sciences and Materials (CIIM) Department at the University of Puerto Rico, Mayaguez Campus (UPRM). Dr. Santiago earned a BA and MS in Industrial Engineering from UPRM and Ph.D. in Engineering Education from Purdue University. Dr. Santiago has over 20 years of experience in academia and is currently the Department Head of the CIIM Department. Her primary research interests are investigating students’ understanding of difficult concepts in engineering science, especially for underrepresented populations. She also works in the development and evaluation of various engineering curriculum and courses at UPRM.